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APPLICANTS

Pradip Mandal, Bangalore, INDIA;

** **CONTINUING DATA** **************************

This application is a 371 of PCT/IB02/05484 12/12/2002

AT

** **FOREIGN APPLICATIONS** ********************

EUROPEAN PATENT OFFICE (EPO) 02075258.0 01/22/2002

Foreign Priority claimed ☒ yes ☐ no 35 USC 119 (a-d) conditions met ☒ yes ☐ no ☐ Met after Allowance Verified and Acknowledged _____ Examiner's Signature _____ Initials *AT*	STATE OR COUNTRY INDIA	SHEETS DRAWING 1	TOTAL CLAIMS 8	INDEPENDENT CLAIMS 1

ADDRESS

24737

TITLE

Voltage limiting semiconductor pass gate circuit

FILING FEE RECEIVED 920	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	☐ All Fees
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